Electronic Acknowledgement Receipt				
EFS ID:	2766377			
Application Number:	10070091			
International Application Number:				
Confirmation Number:	7309			
Title of Invention:	Register set used in multithreaded parallel processor architecture			
First Named Inventor/Applicant Name:	Gilbert Wolrich			
Customer Number:	20985			
Filer:	Denis G. Maloney/Diane Saturno			
Filer Authorized By:	Denis G. Maloney			
Attorney Docket Number:	10559-310US1			
Receipt Date:	25-JAN-2008			
Filing Date:	28-JUN-2002			
Time Stamp:	12:06:06			
Application Type:	U.S. National Stage under 35 USC 371			
Payment information:				
Submitted with Payment	по			

File Listing:						
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)	
1 Applicant summary of inter examiner	Applicant summary of interview with	10559310US1intsum.pdf	97285	no	2	
	examiner		fa97b915e585ed3a7ed6256ce29d42fbd Sacbd1b			

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the International filing date of the application.